

FIG. 1

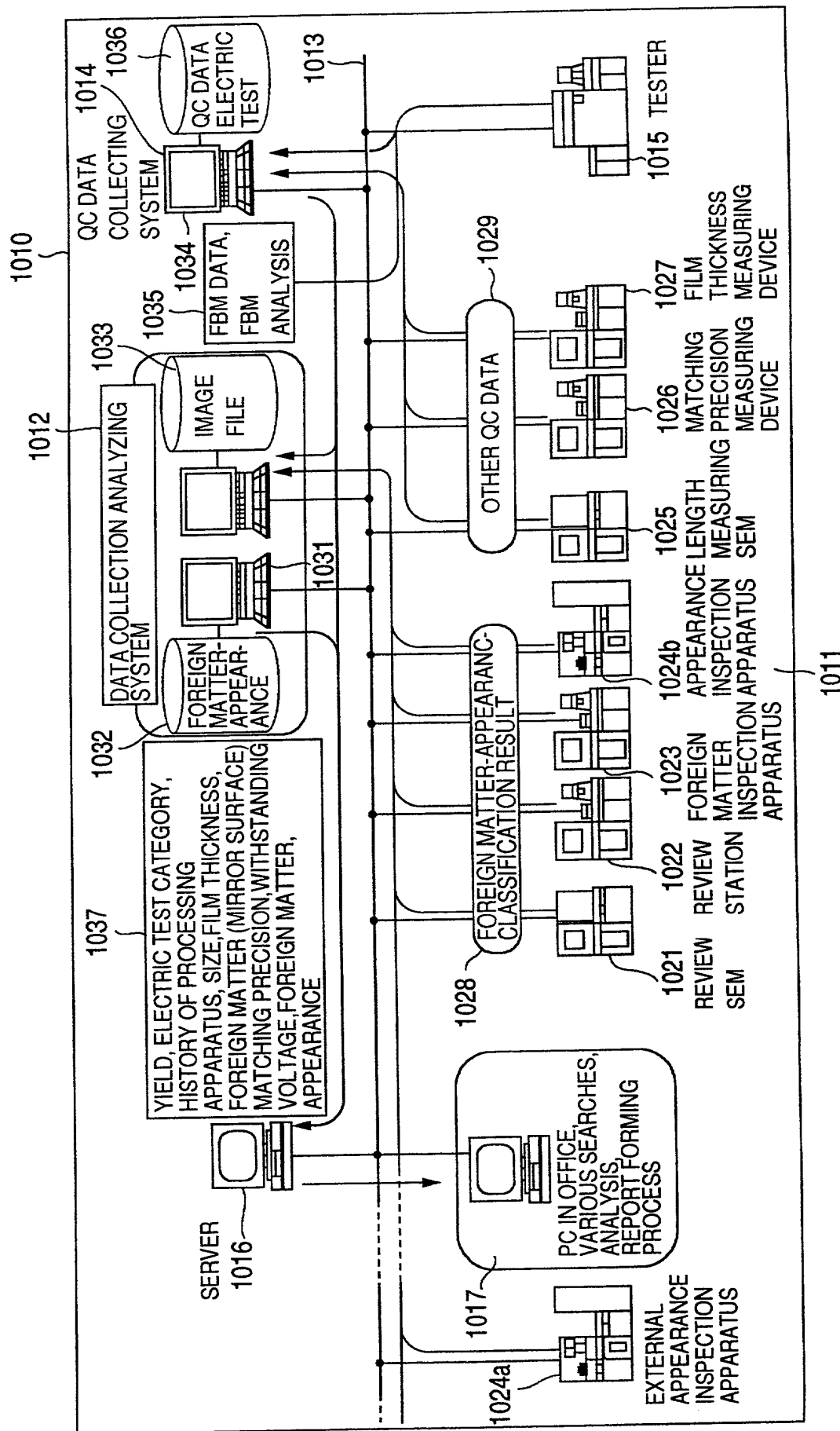


FIG.2

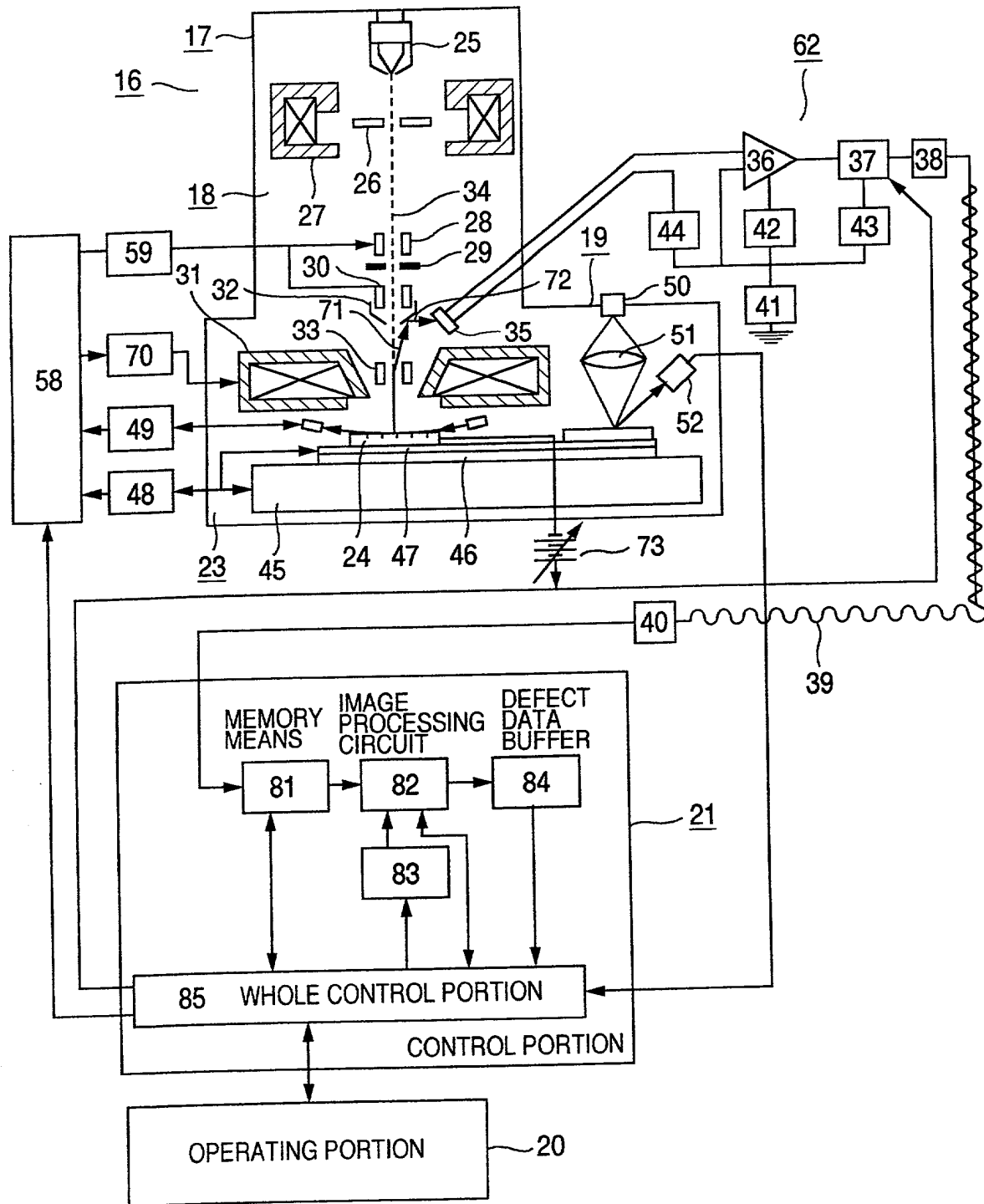


FIG.3

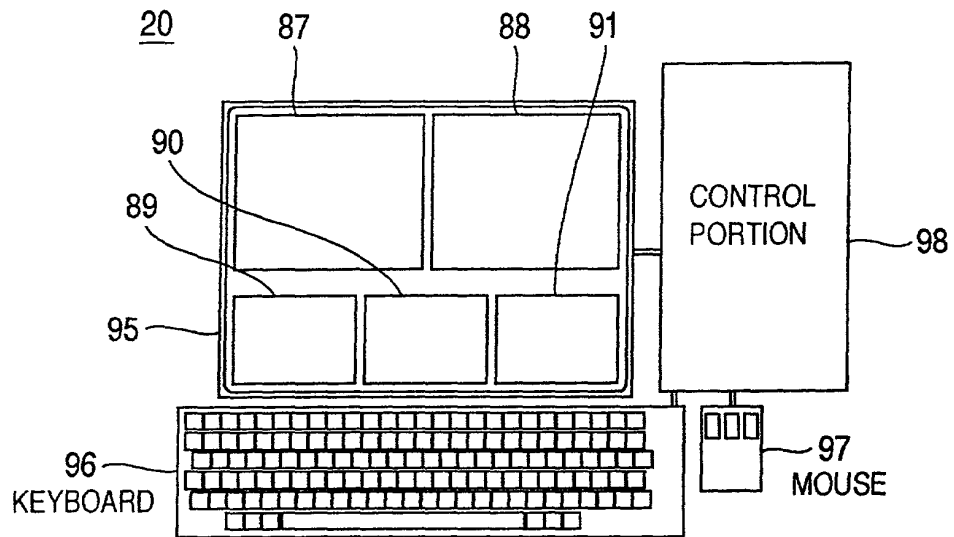


FIG.4

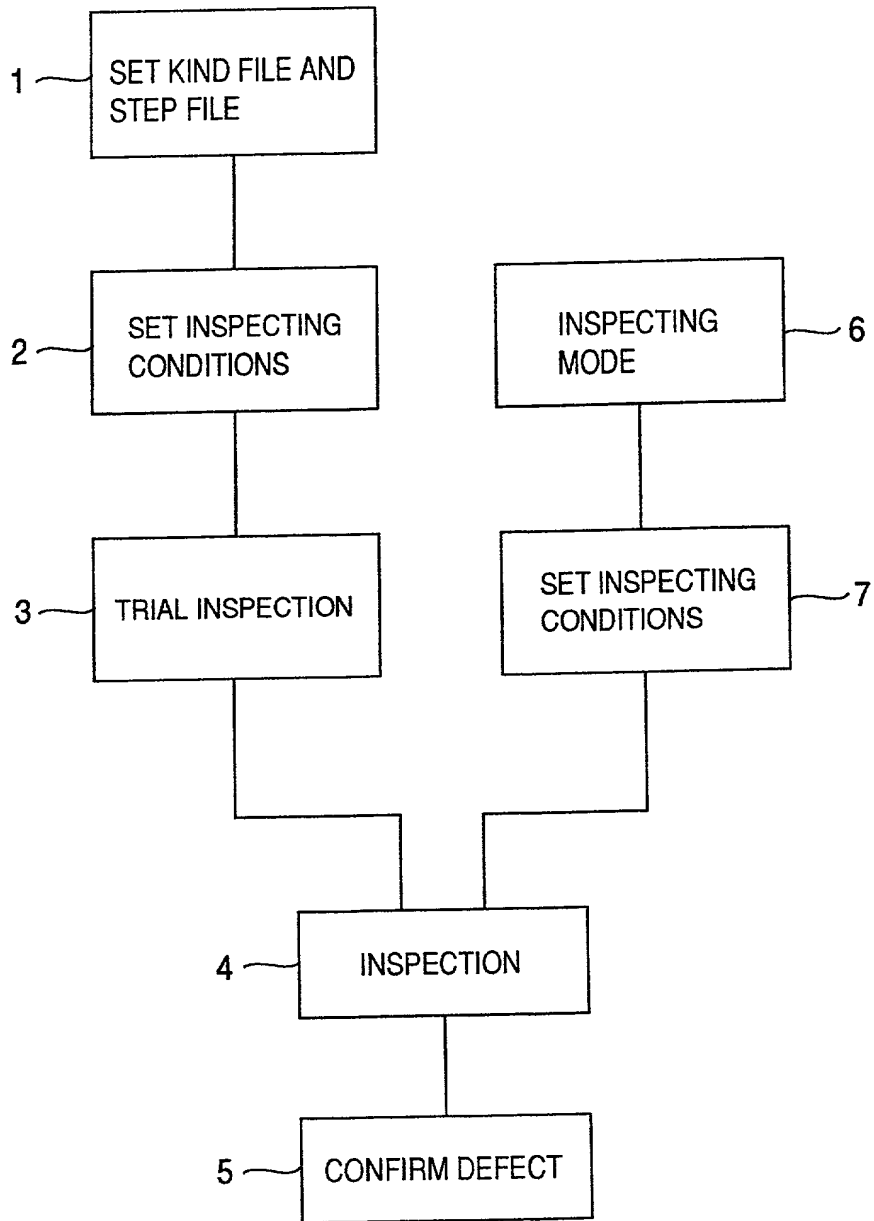


FIG.5

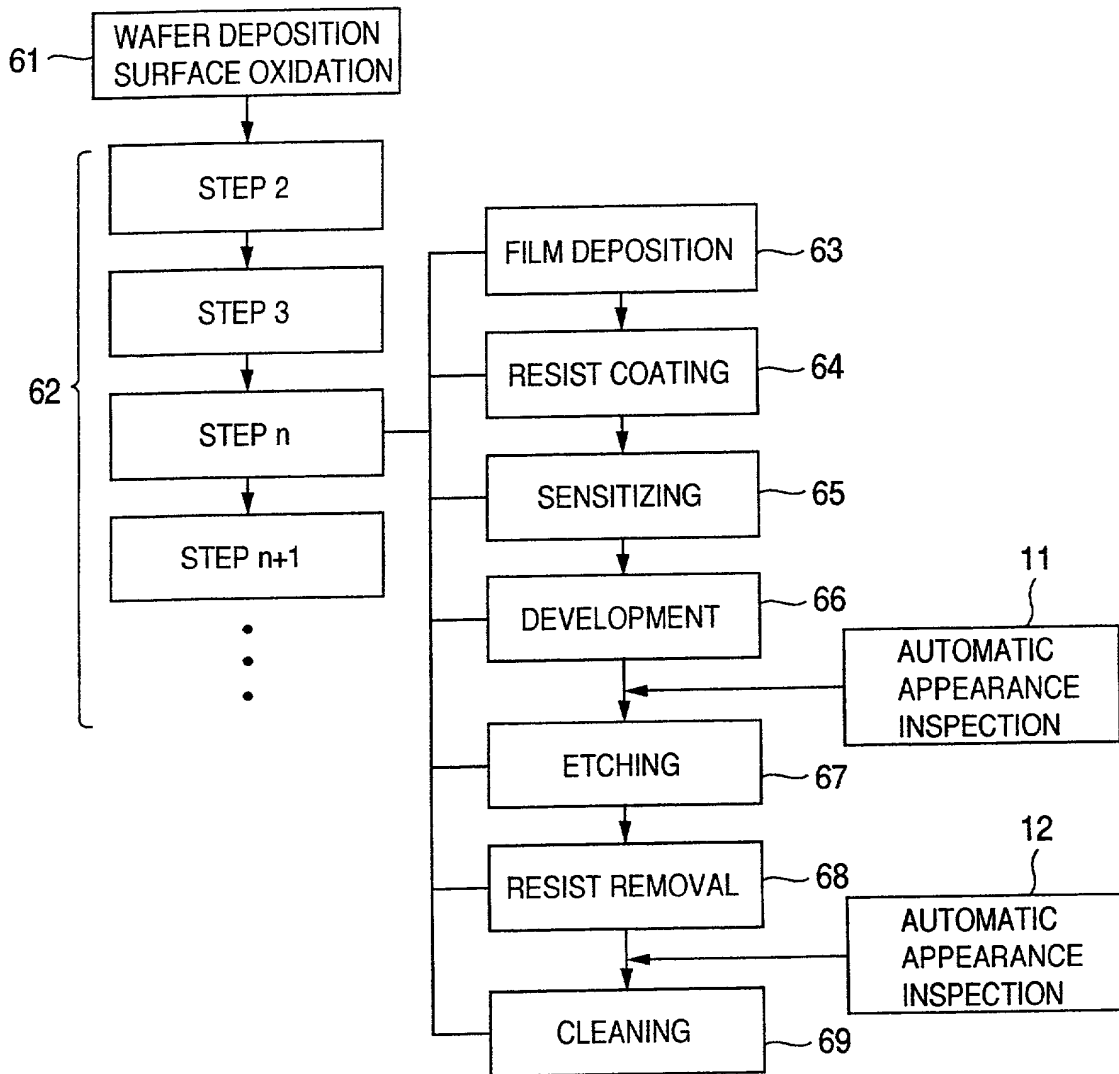


FIG.6

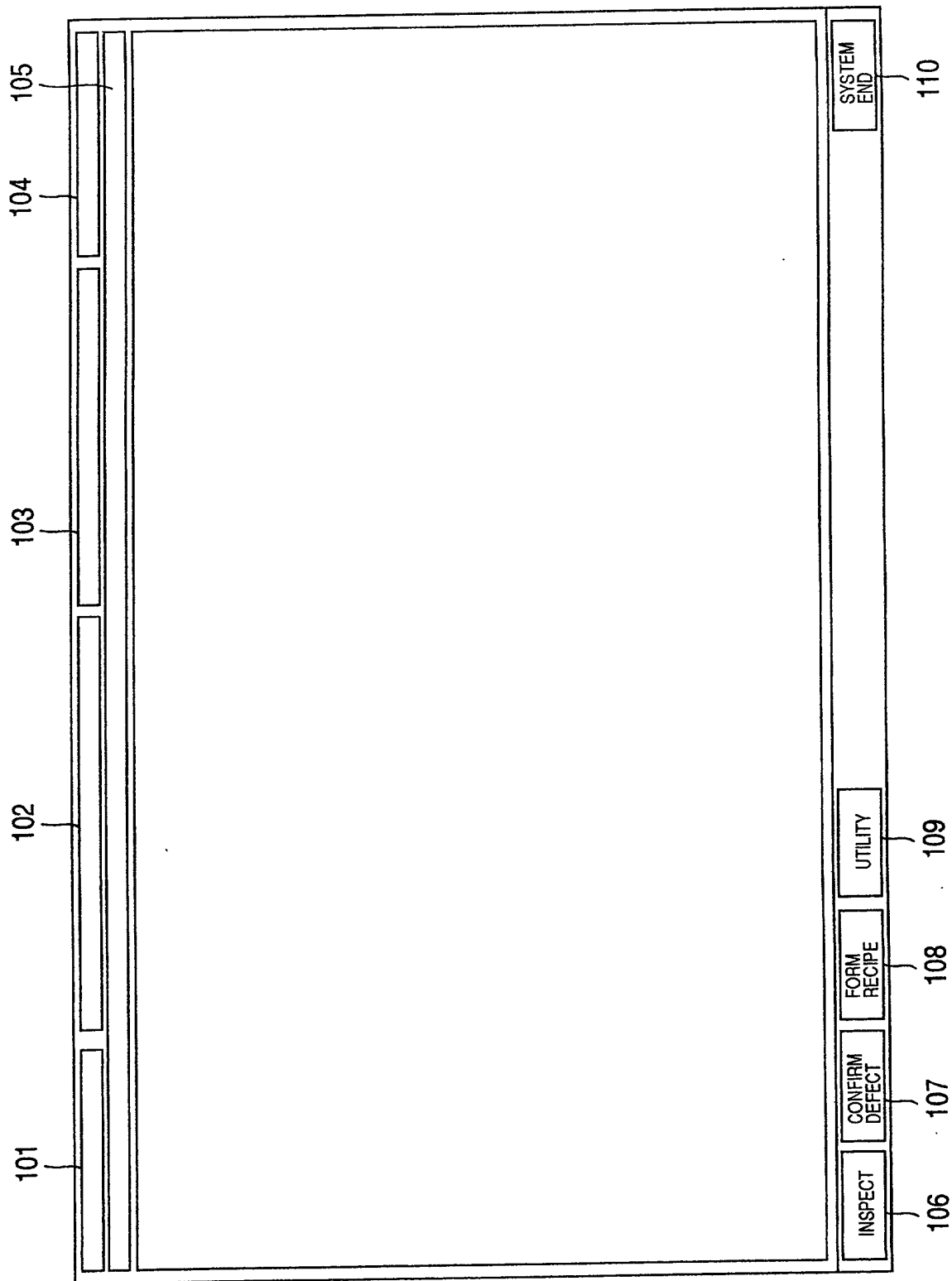


FIG.7

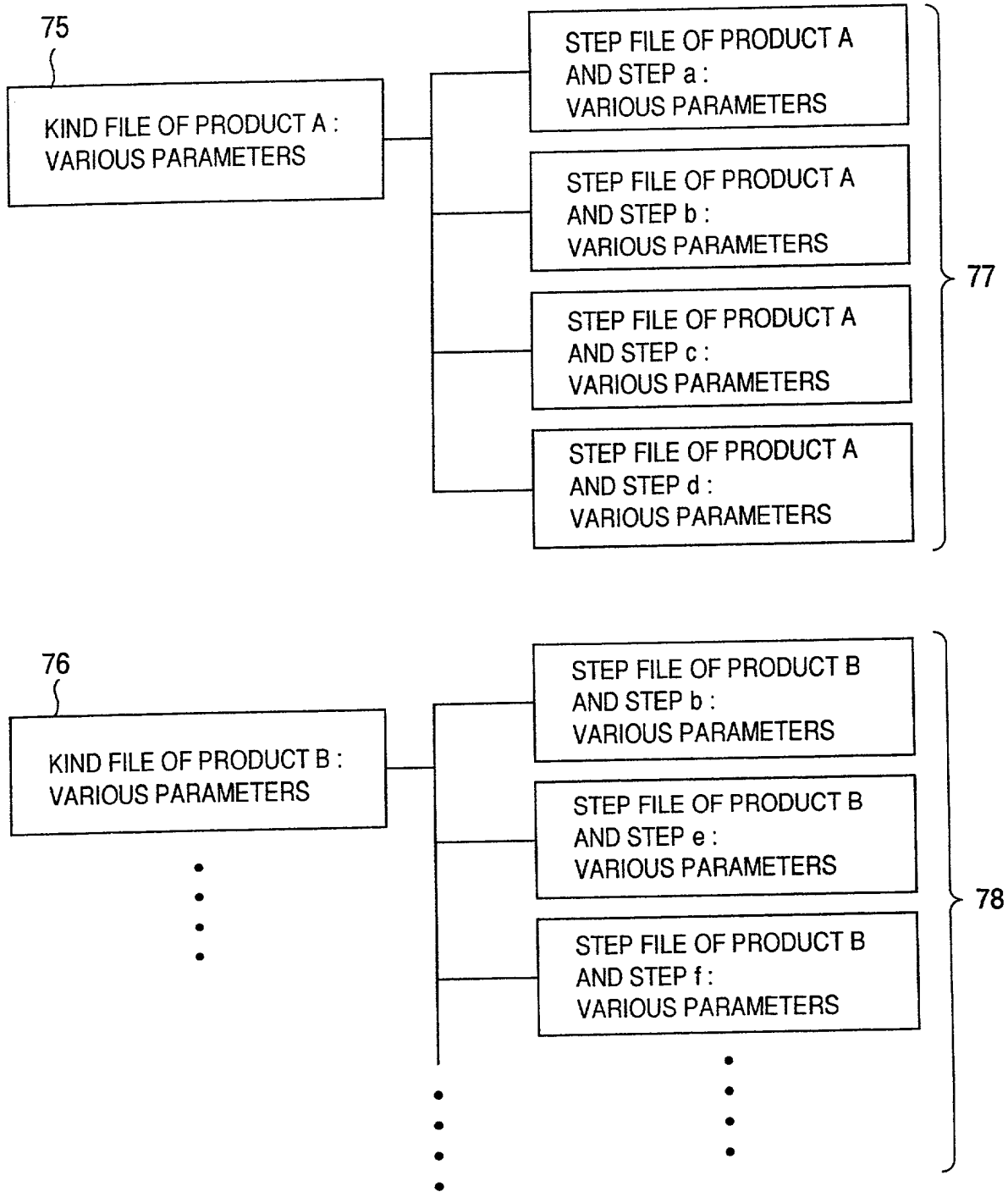


FIG.8

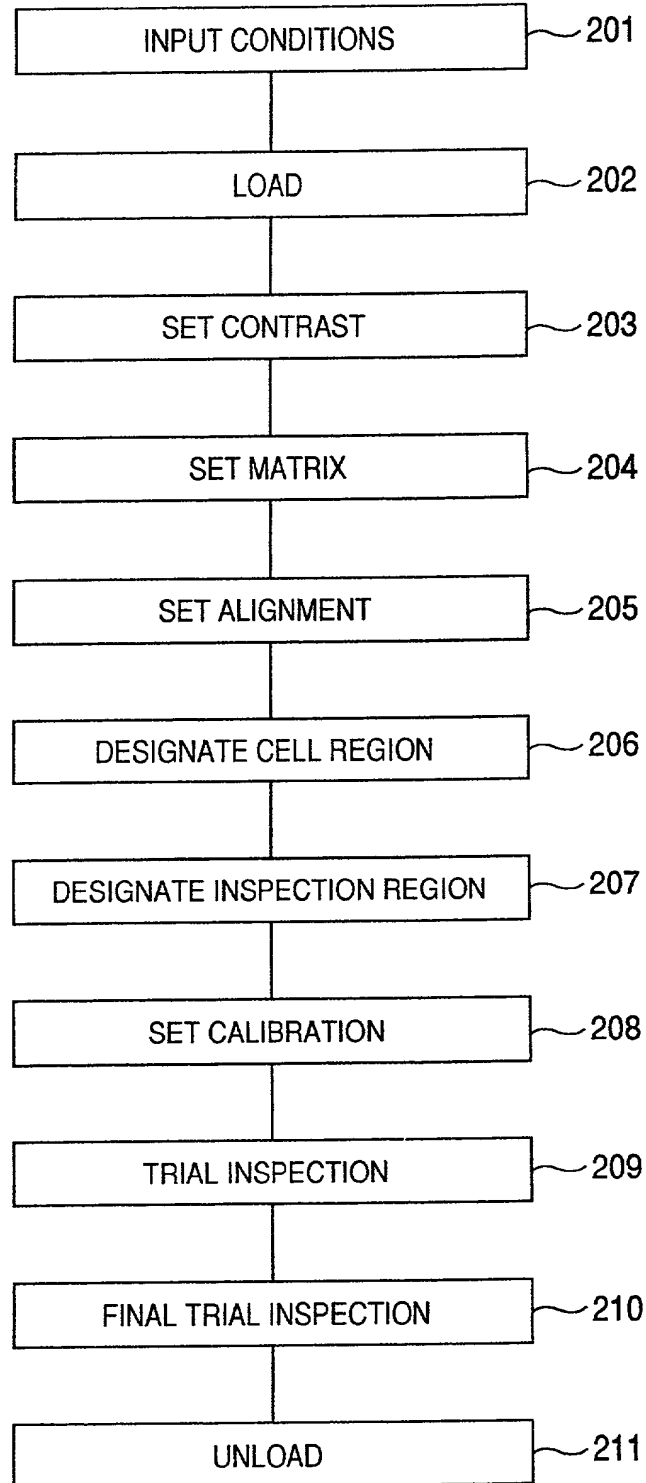


FIG.9

105

APPARATUS NAME +
APPARATUS ID

RECIPE NAME

SET RECIPE FORMING CONDITIONS

INPUT CONDITIONS

LOAD WAFER

BEAM CALIBRATION

CONTRAST

CHIP MATRIX

ALIGNMENT

CELL INFORMATION

INSPECTION REGION

CALIBRATION

TRIAL INSPECTION

FINAL TRIAL INSPECTION

A

B

25	
24	
23	
22	
21	
20	
19	
18	
17	
16	
15	
14	
13	
12	
11	
10	
09	
08	
07	
06	
05	
04	
03	
02	
01	

KIND

STEP

113

114

111

112

116

FORMING PROCEDURE

LOAD WAFER

ONLY INSPECTION CHIP

FORM RECIPE

END

106

107

108

109

INSPECT

CONFIRM DEFECT

FORM RECIPE

UTILITY

115

SYSTEM END

ELECTRON BEAM
IRRADIATING CONDITIONS
◆ LOW ACCELERATION VOLTAGE MODE
◆ HIGH ACCELERATION VOLTAGE MODE

FIG.10

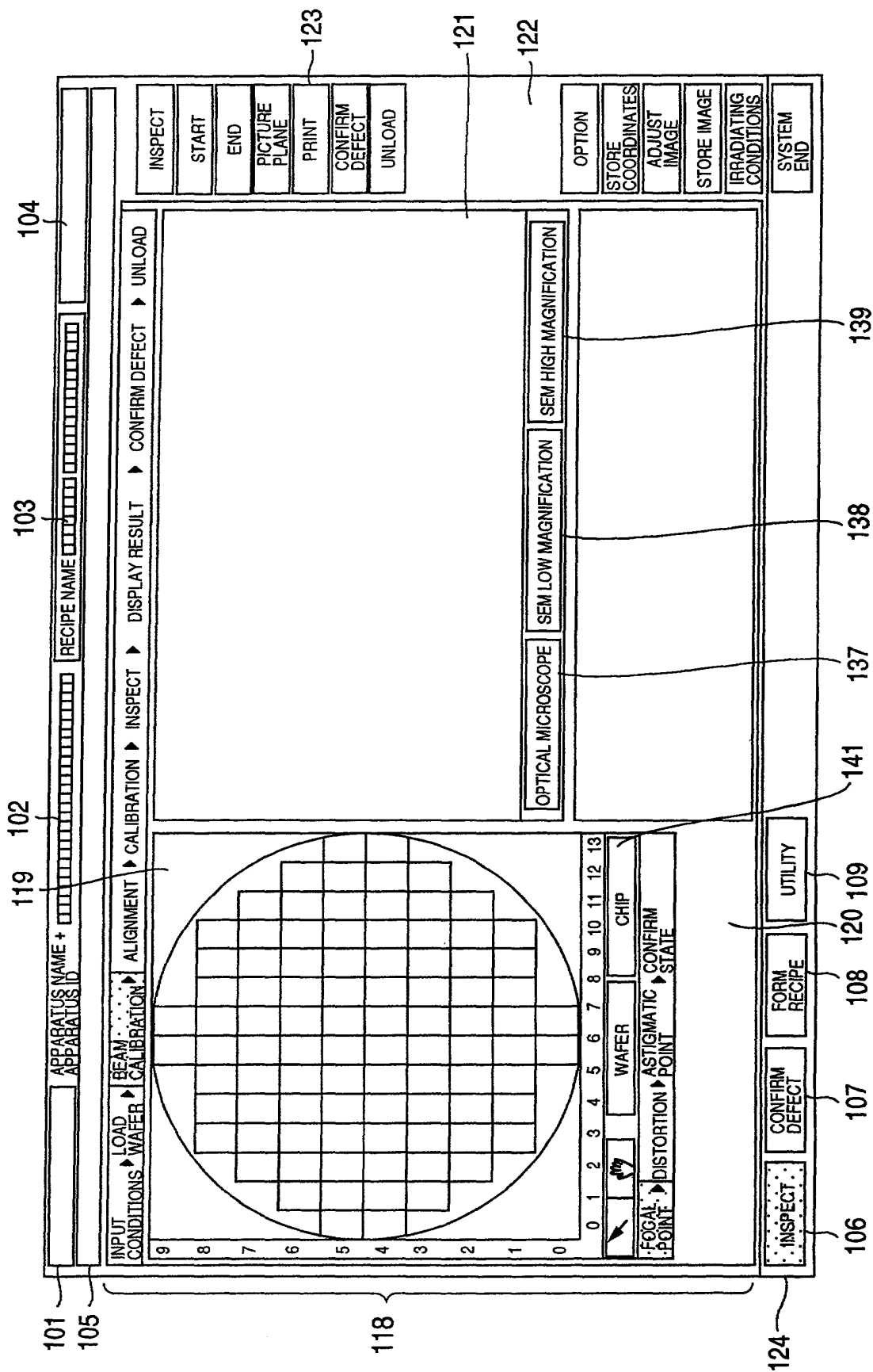


FIG.11

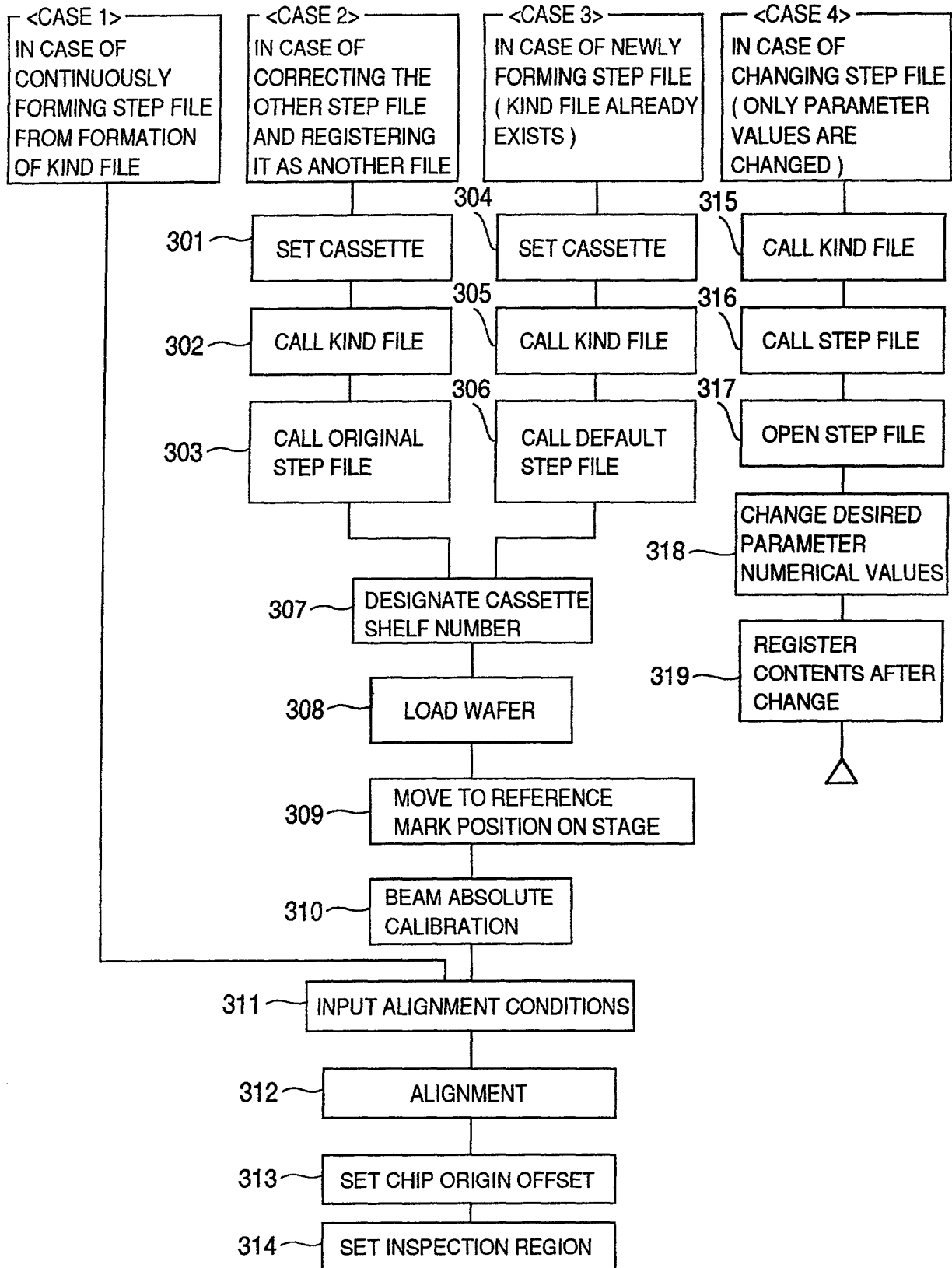


FIG.12

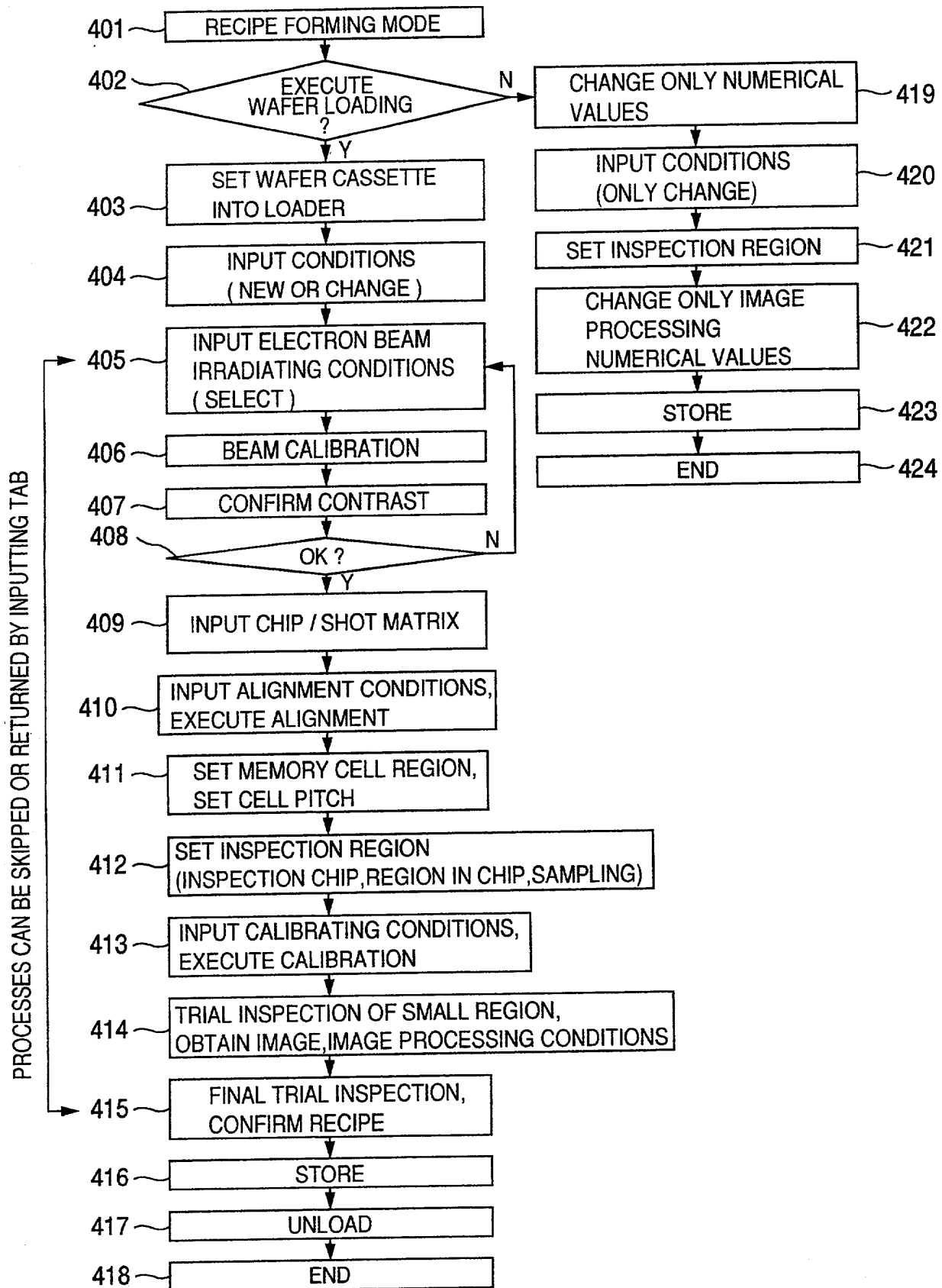


FIG. 13

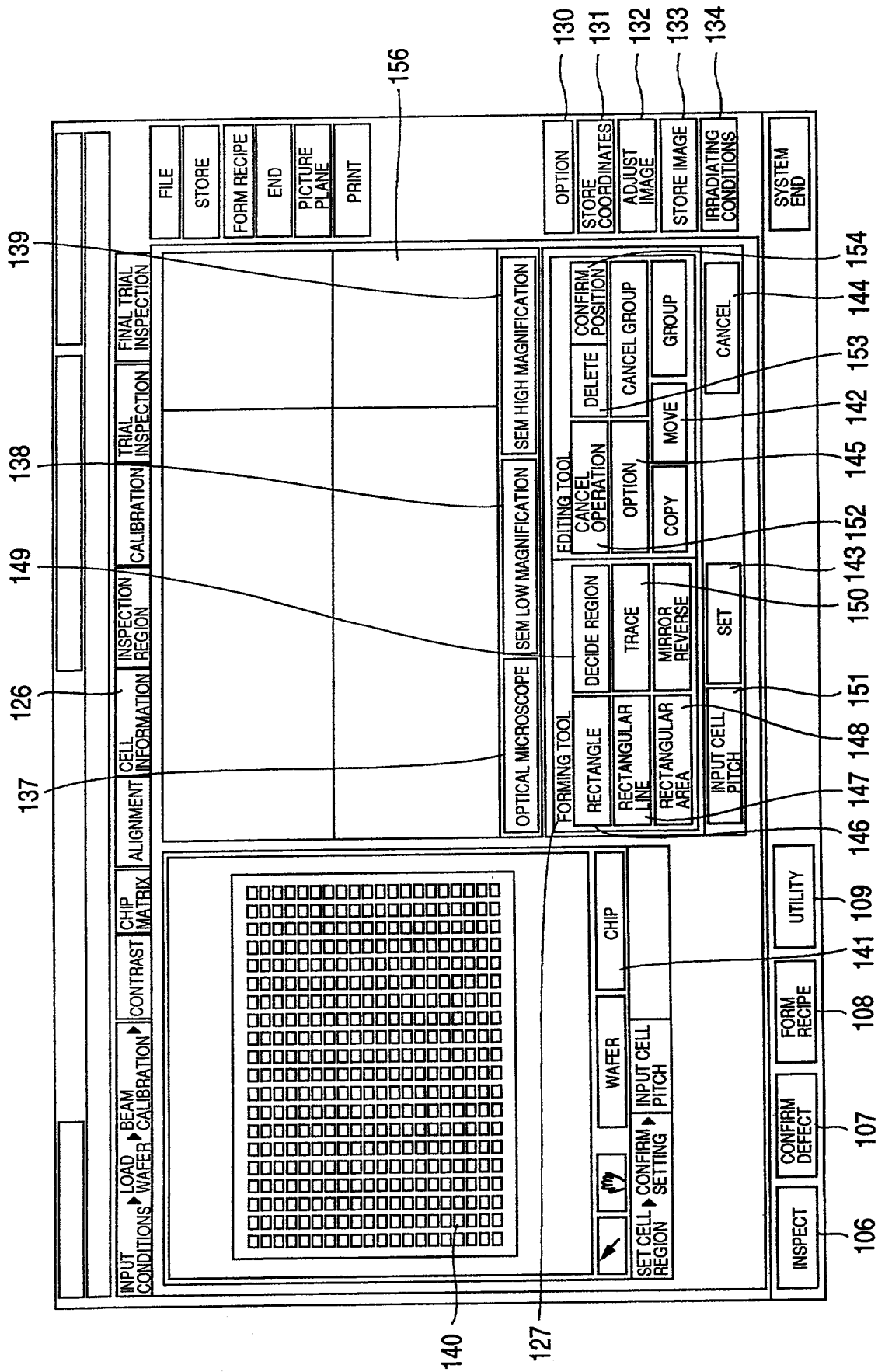


FIG.14

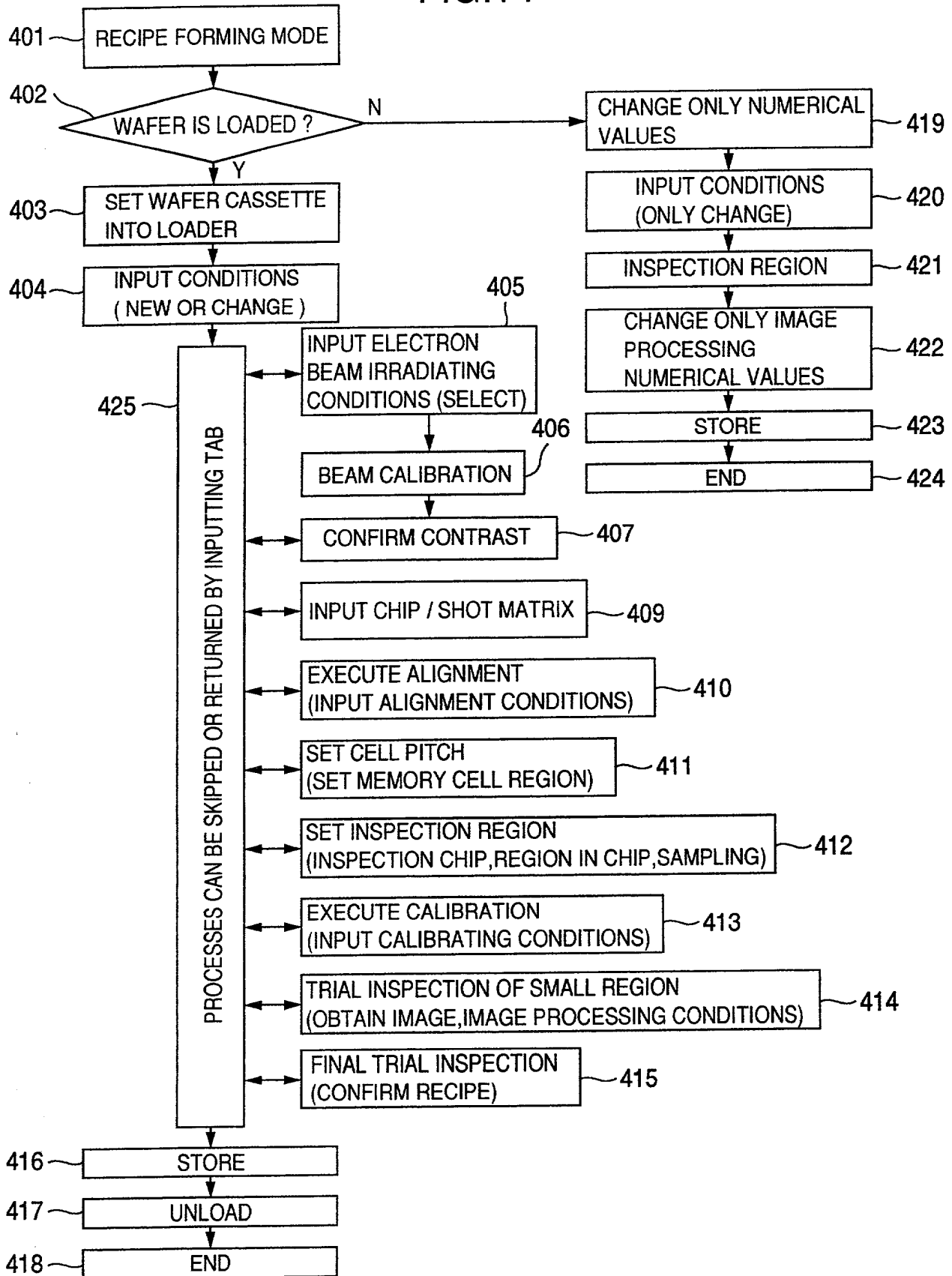


FIG.15

IRRADIATING CONDITIONS

ELECTRON BEAM IRRADIATING CONDITIONS

ACCELERATION VOLTAGE ▾ V

BEAM CURRENT ▾ nA

OBTAIN SIGNAL

THE NUMBER OF SIGNAL
ADDING TIMES ▾ ☐

PIXEL SIZE ▾ μm

SET CANCEL

92

93

94

FIG.16

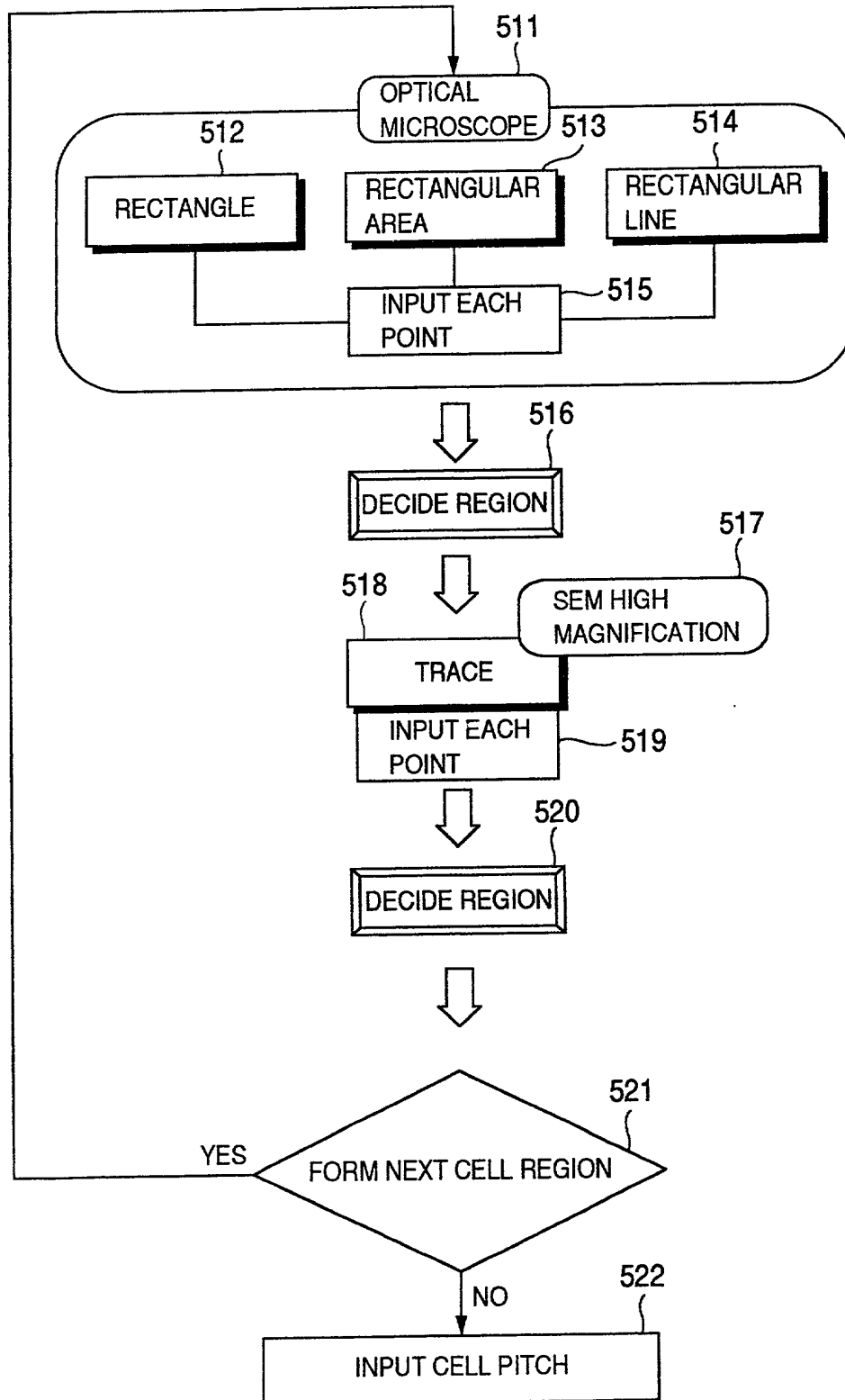


FIG.17

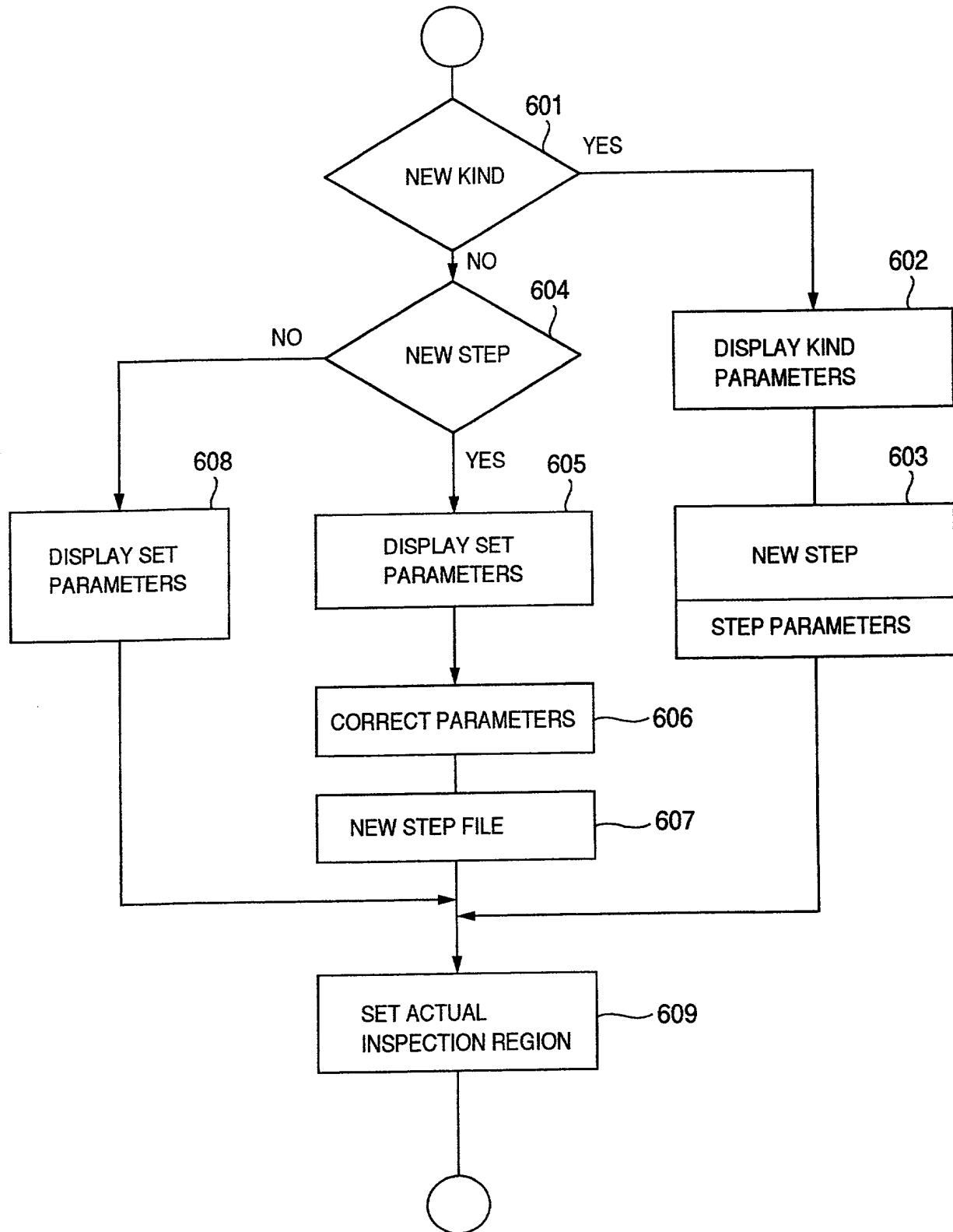


FIG.18

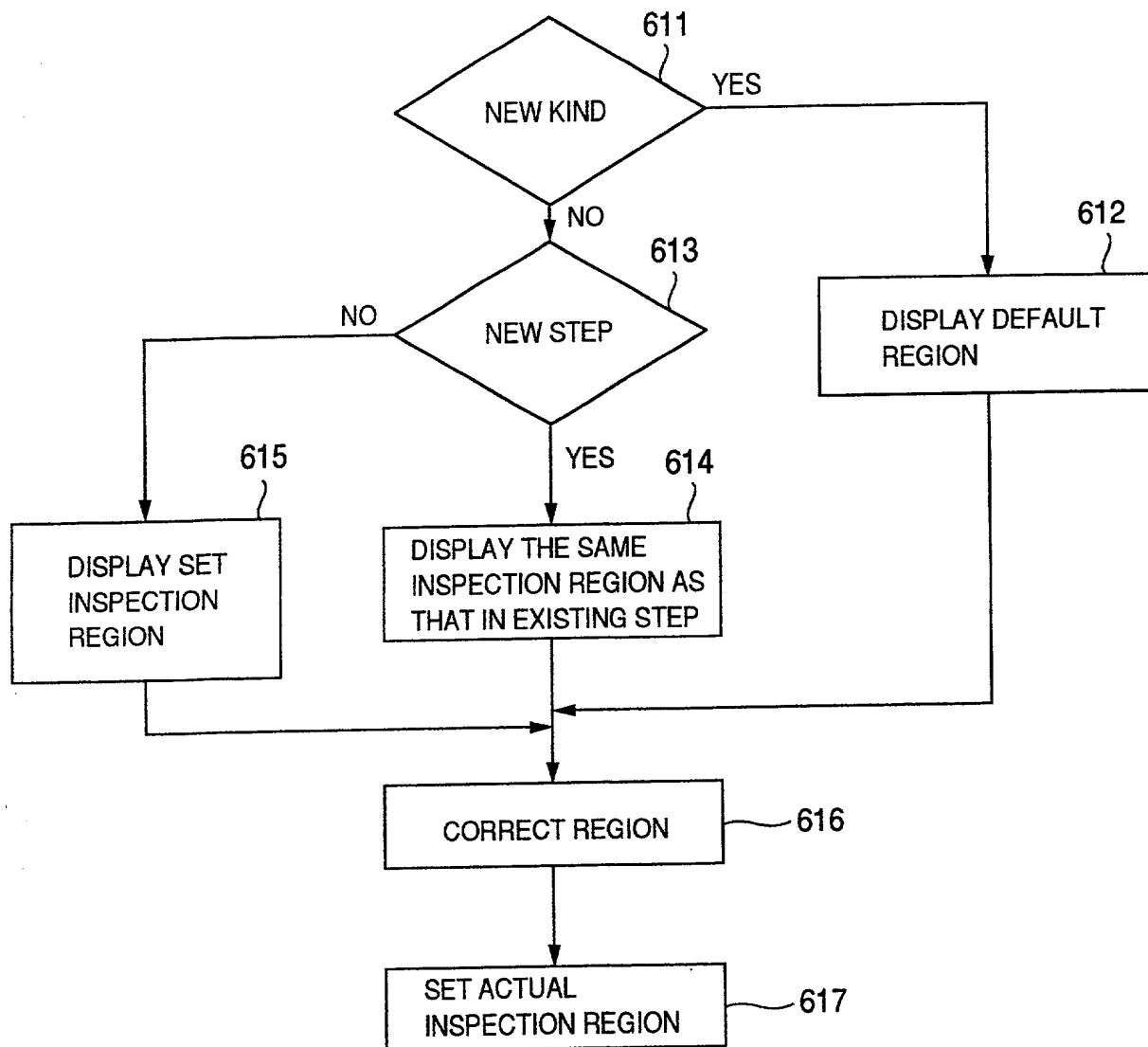


FIG.19

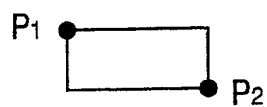


FIG.20

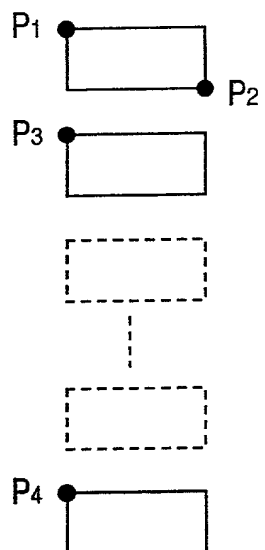


FIG.21

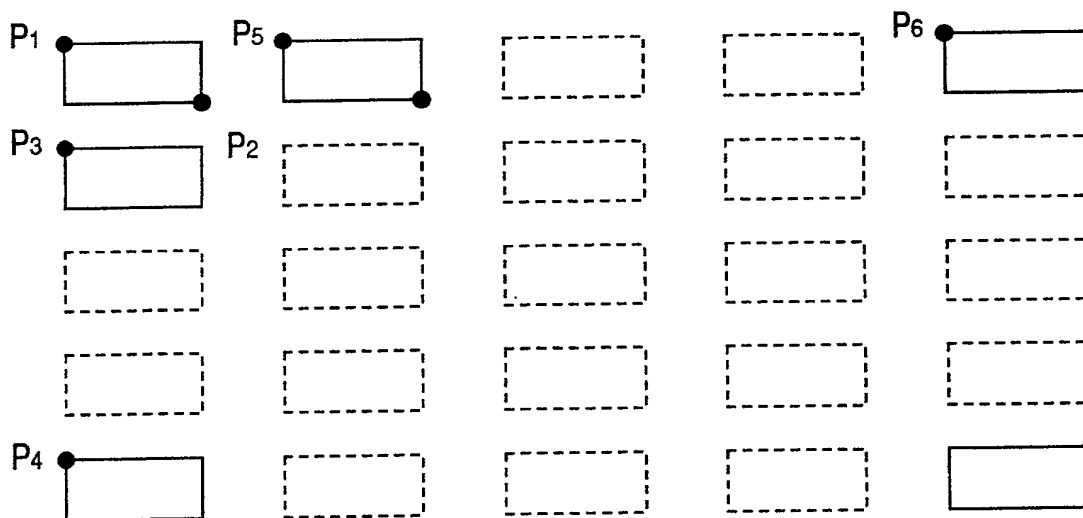


FIG.22

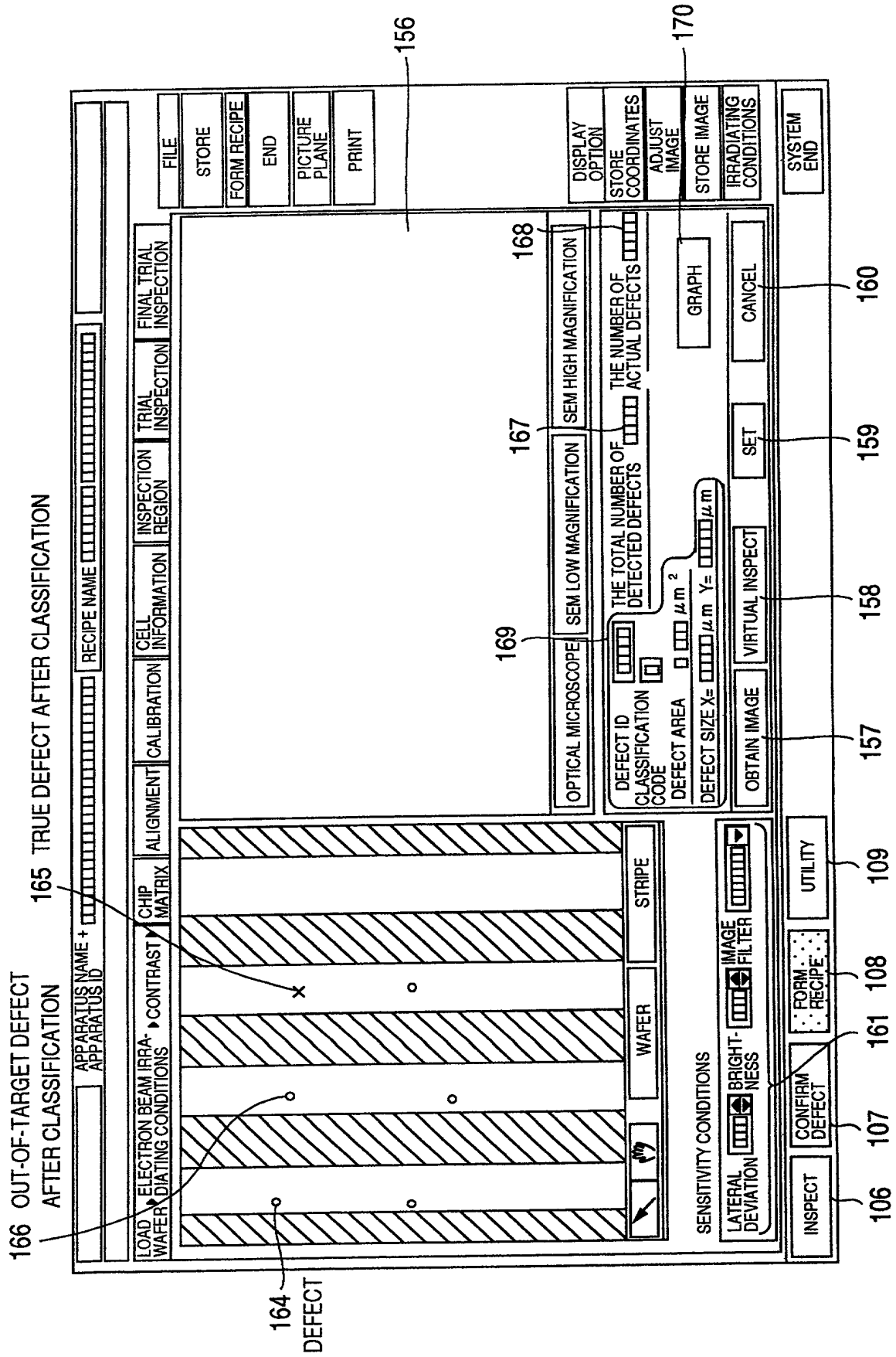


FIG.23

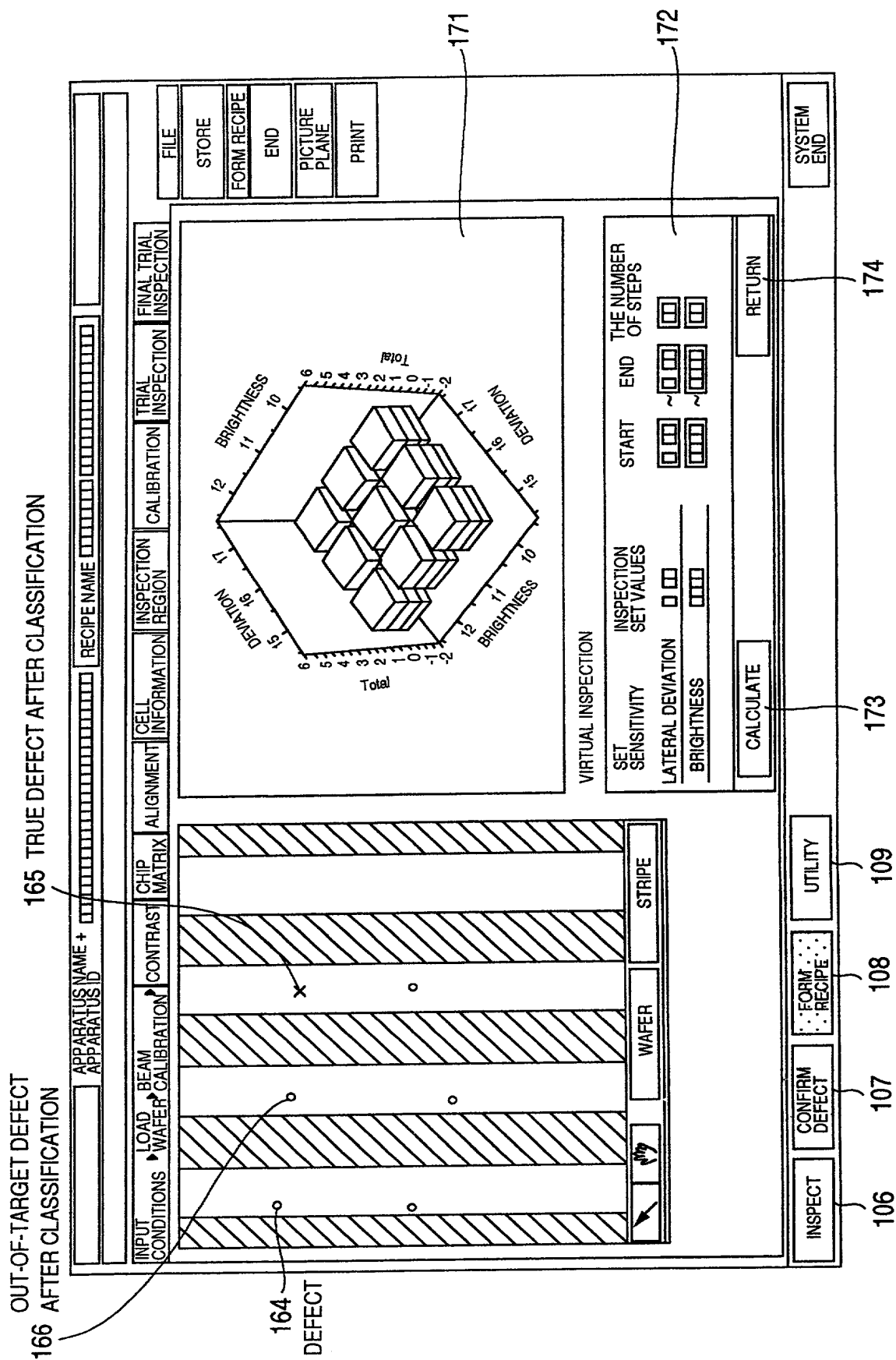
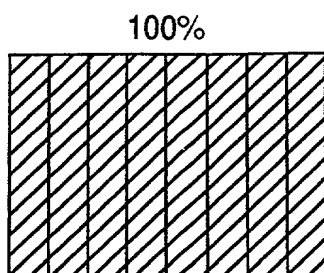
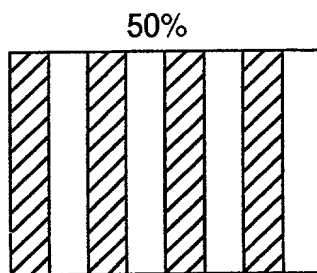


FIG.24A



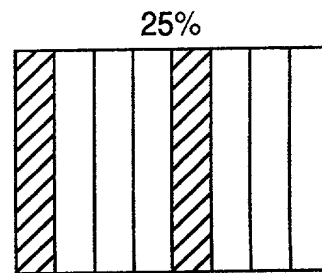
SCAN ALL
STRIPE

FIG.24B



SCAN EVERY
OTHER STRIPE

FIG.24C



SCAN EVERY
FOURTH STRIPE

FIG.25

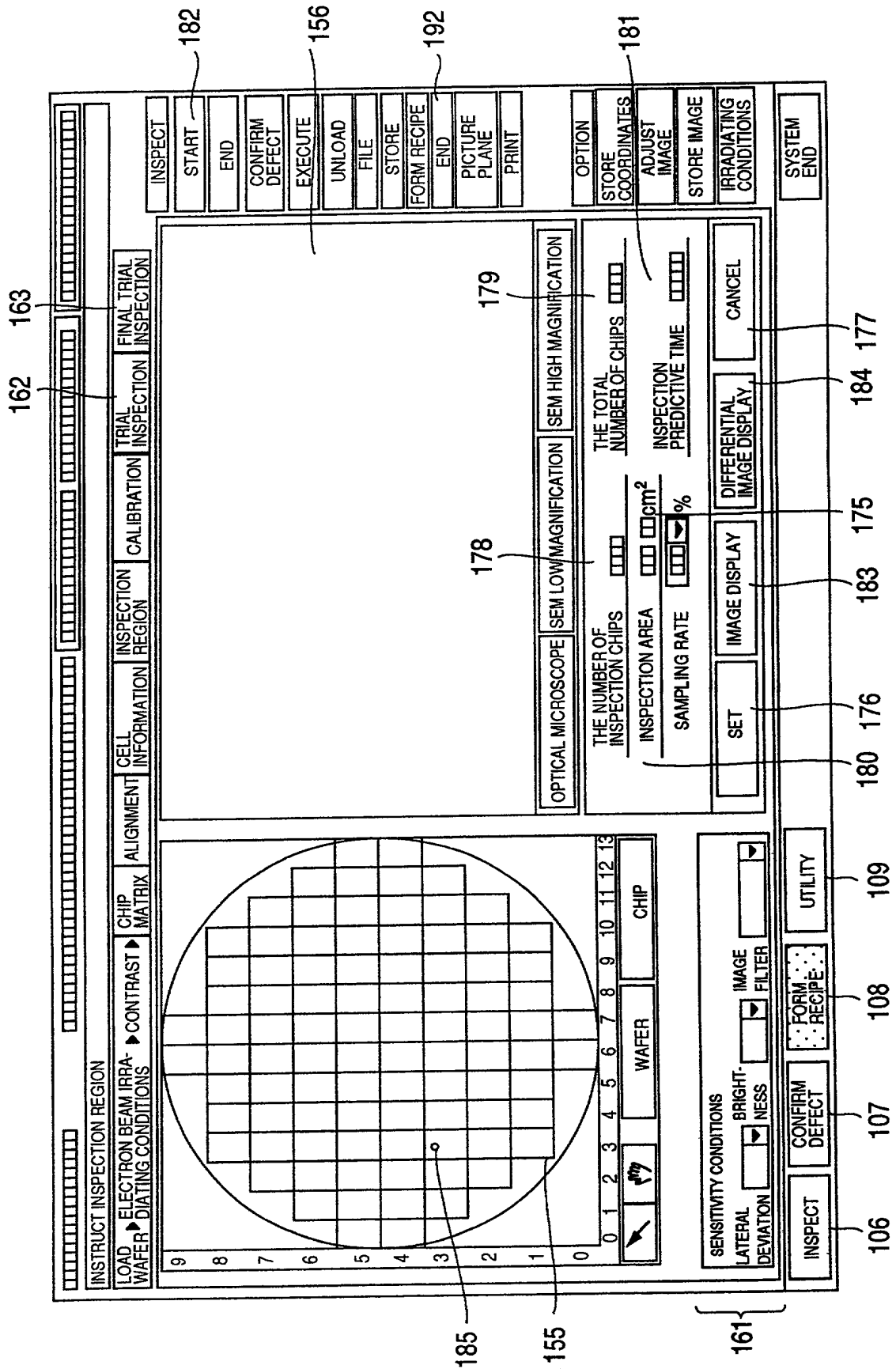


FIG.26

1988/11/06 15:00 IX_000_ID000V00.R03

R_TEST:KatblIX

INPUT CONDITIONS

LOAD

BEAM

WAFER

CALIBRATION

CONTRAST

CHIP MATRIX

ALIGNMENT

CELL INFORMATION

INSPECTION REGION

CALIBRATION

TRIAL INSPECTION

FINAL TRIAL INSPECTION

18

17

16

15

14

13

12

11

10

9

8

5

6

7

8

9

10

11

12

13

14

15

18

17

16

15

14

13

12

11

10

9

8

COORDINATES X IN CHIP

COORDINATES Y IN CHIP

CHIP MATRIX X

CHIP MATRIX Y

CLASSIFICATION CODE

WAFER

CHIP

DECIDE-DISPLAY AGAIN

EXCHANGE

INITIALIZE NUMERICAL VALUES

THE NUMBER OF DISPLAY DEFECTS

INSPECT

CONFIRM DEFECT

FORM RECIPE

UTILITY

INSPECT

START

END

CONFIRM DEFECT

EXECUTE

UNLOAD

FILE

STORE

FORM RECIPE

END

PICTURE PLANE

PRINT

OPTION

STORE COORDINATES

ADJUST IMAGE

STORE IMAGE

IRRADIATING CONDITIONS

SYSTEM END

Sum

SEM LOW MAGNIFICATION

SEM HIGH MAGNIFICATION

OPTICAL MICROSCOPE

SHELF NUMBER B25

KIND R_TEST

STEP Katbl

WAFER ID LOT ID

OPERATOR

191

187

186

188

156

192

130

131

132

133

134

193

109

108

107

106

FIG.27

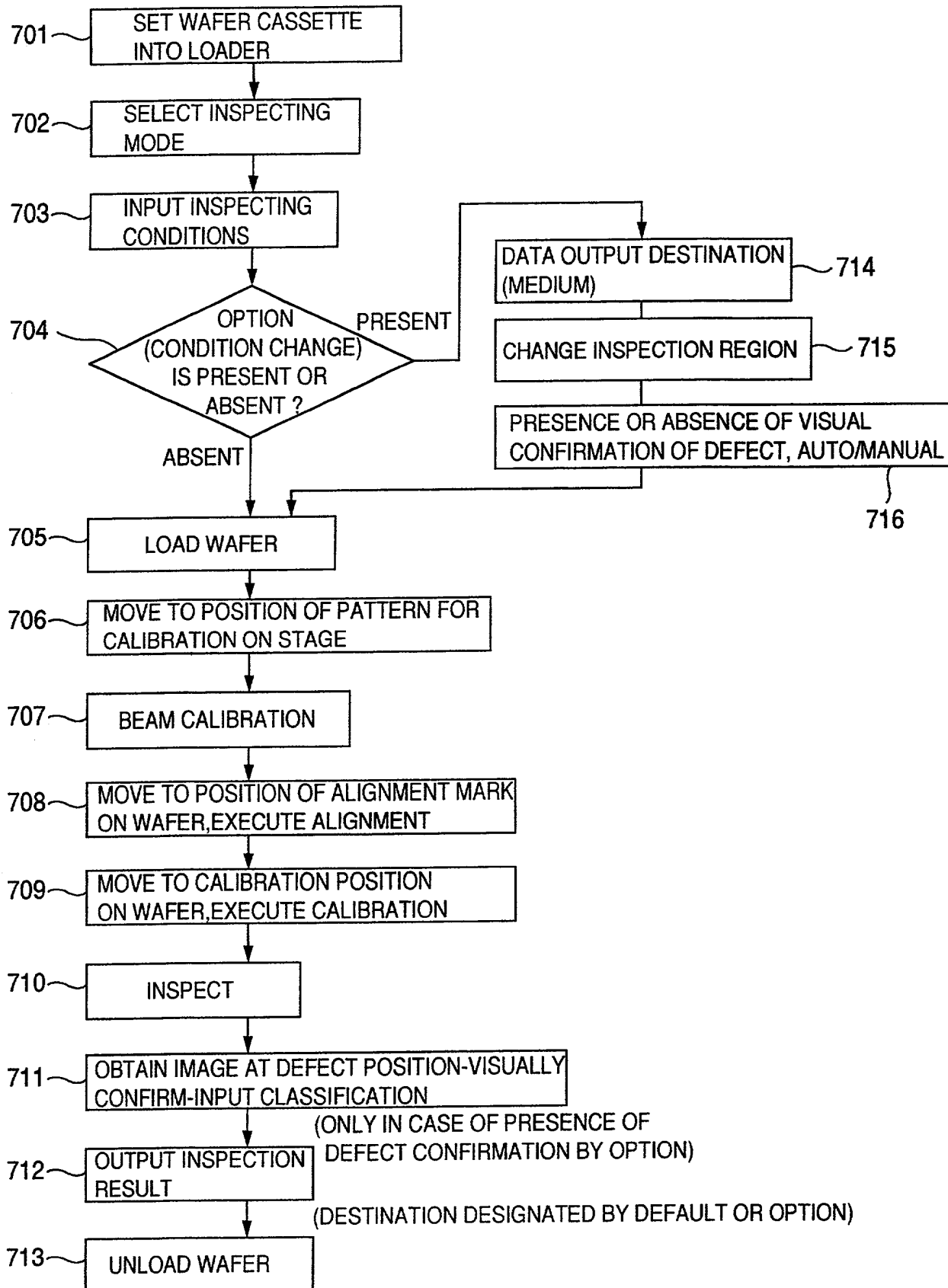


FIG.28

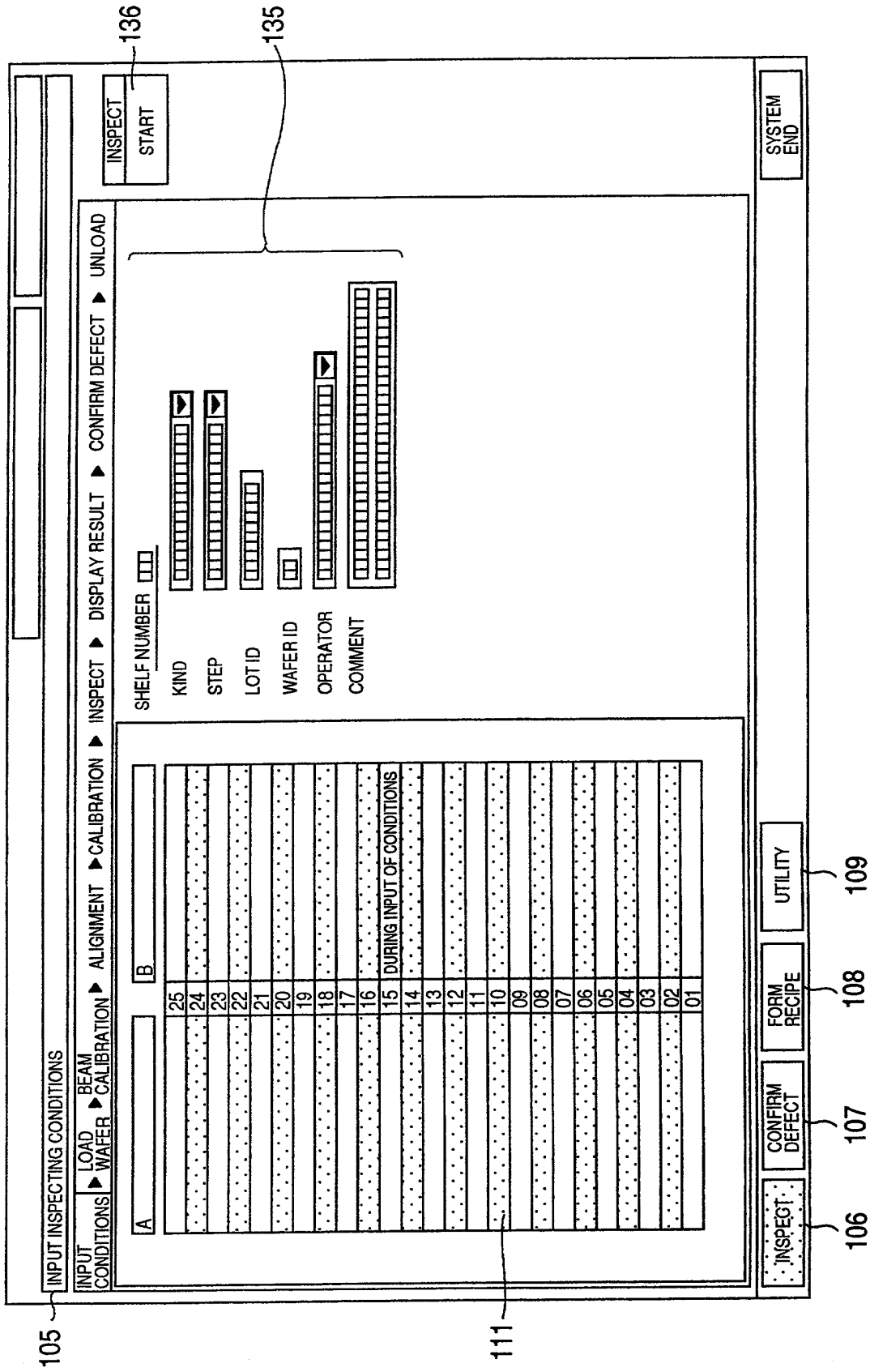


FIG.30

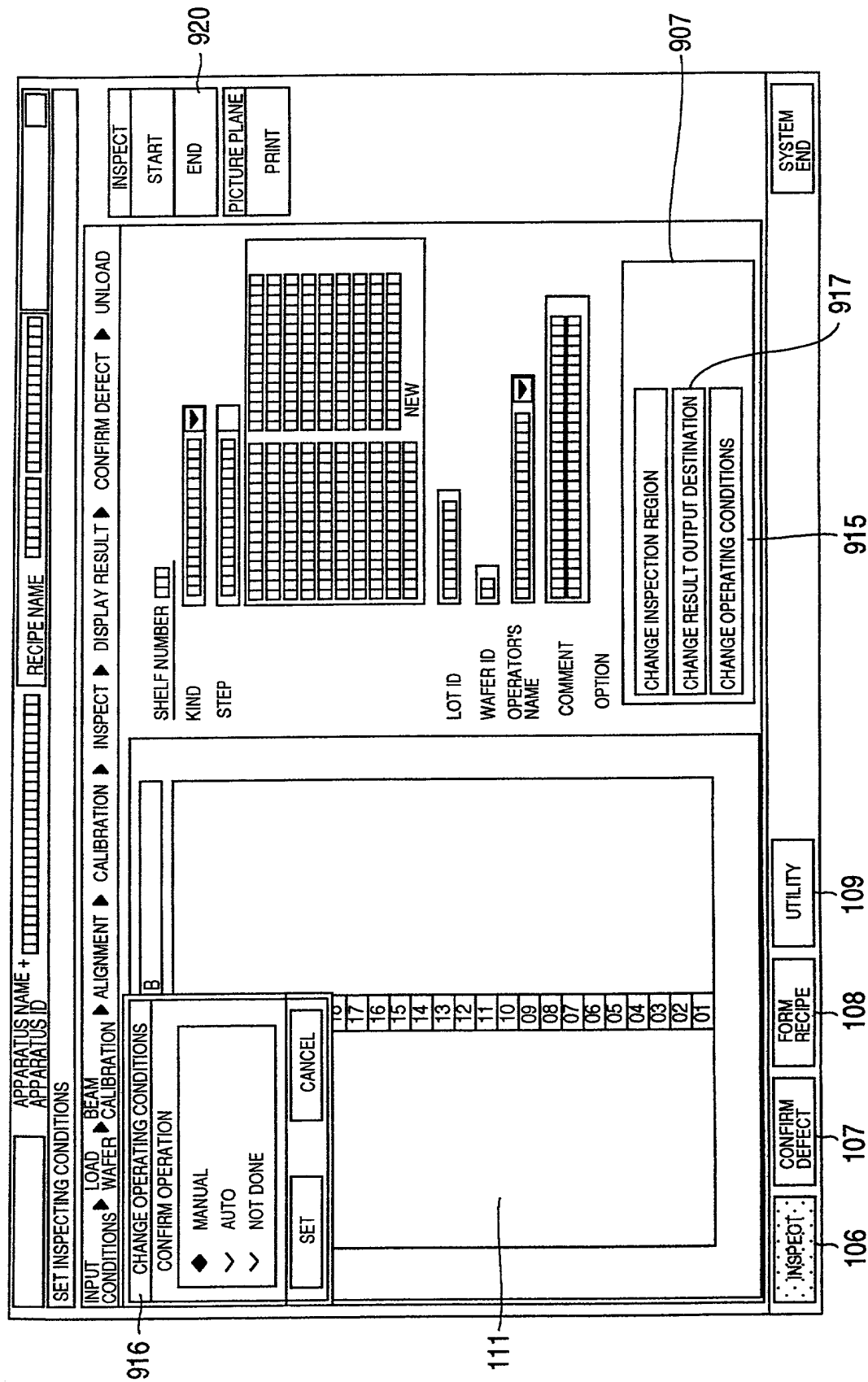


FIG.31

APPARATUS NAME + APPARATUS ID

RECIPE NAME

SET INSPECTING CONDITIONS

INPUT CONDITIONS

LOAD

BEAM

WAFER

CALIBRATION

ALIGNMENT

INSPECT

DISPLAY RESULT

CONFIRM DEFECT

UNLOAD

CHANGE RESULT OUTPUT DESTINATION

SHELF NUMBER

H/D

F/D

MO

AS

PRINTER

STANDARD

OPTION

SET

CANCEL

INSPECT

START

END

PICTURE PLANE

PRINT

SHELF NUMBER

KIND

STEP

LOT ID

WAFER ID

OPERATOR NAME

COMMENT

OPTION

CHANGE INSPECTION REGION

CHANGE RESULT OUTPUT DESTINATION

CHANGE OPERATING CONDITIONS

INSPECT

FORM RECIPE

CONFIRM DEFECT

UTILITY

SYSTEM END

918

920

919

907

917

106

107

108

109

FIG.33

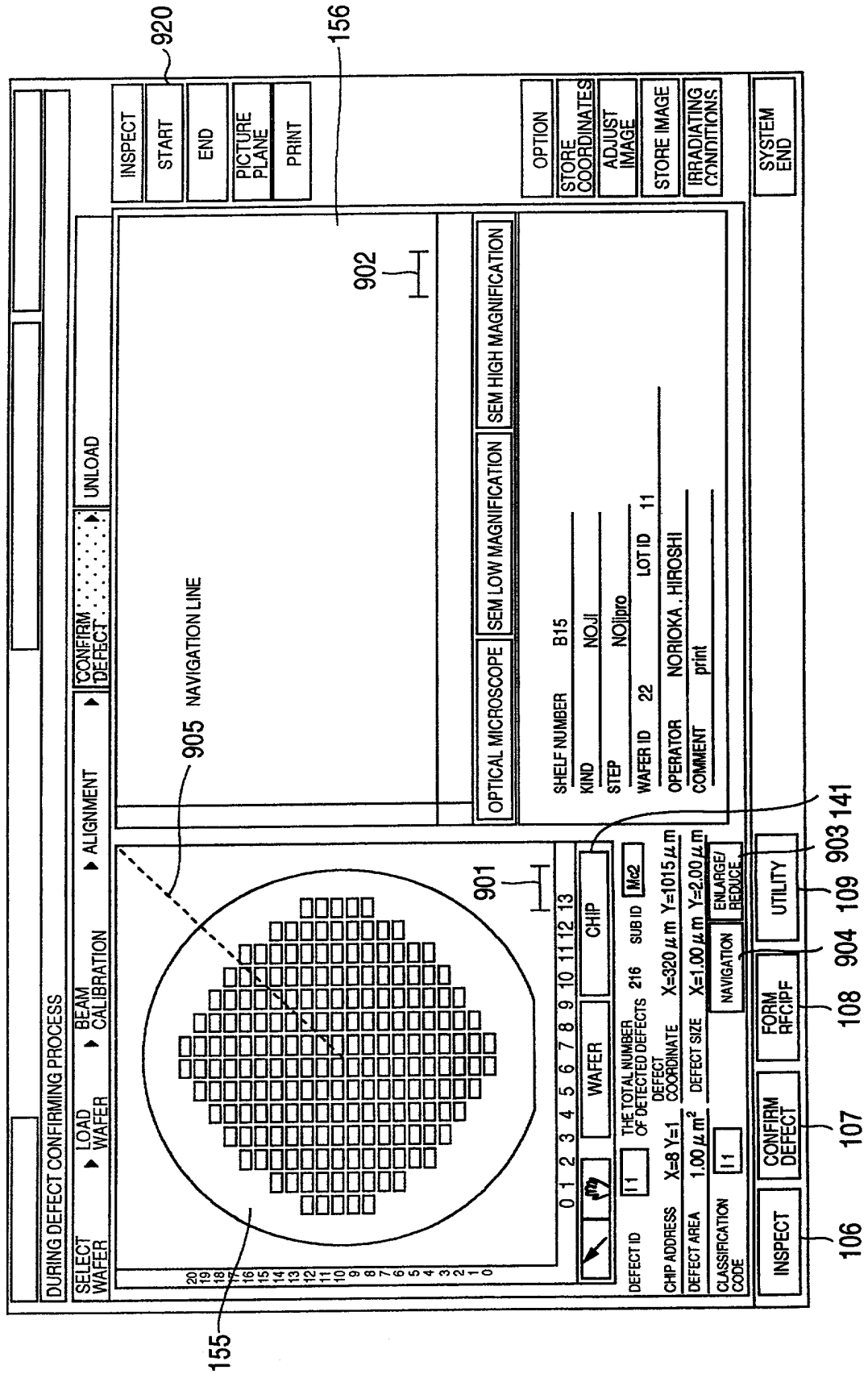


FIG.34

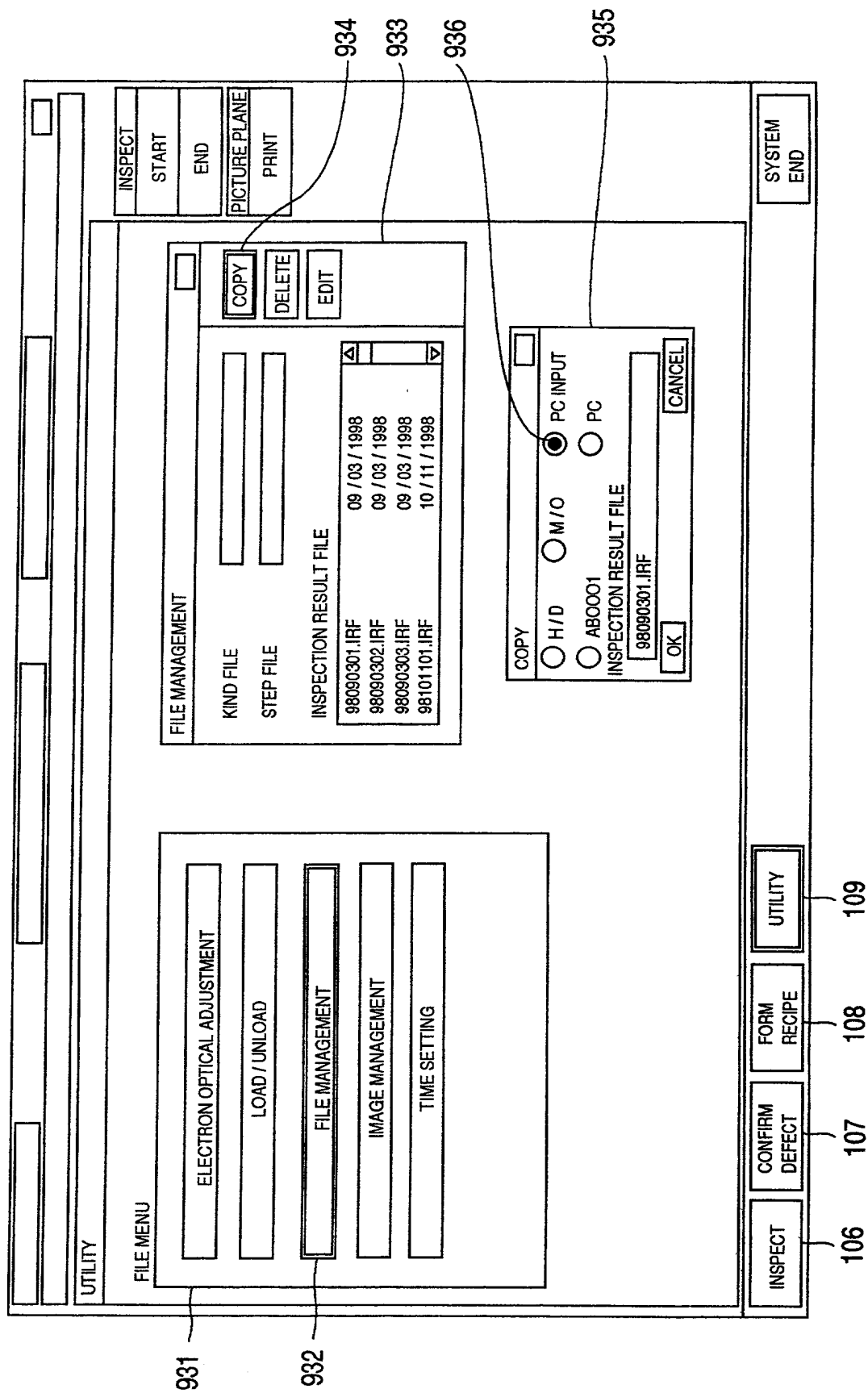


FIG.36

